

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,340,010 B2
APPLICATION NO. : 10/764629
DATED : March 4, 2008
INVENTOR(S) : Brian William Kroeger

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 9, Line 12
 $d_i=17$ and $a=3$. should read $d_i=17$ and $a=3$.

Table 4

Line 53

$\text{colB}(k) = \text{mod}(2 \cdot k, 15)$ should read $\text{colB}(k) = \text{mod}(2 \cdot k, 5)$

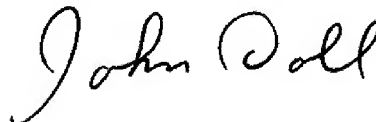
Column 13, Line 12
...dropped and $s_{i,k}^{1,k}$ denotes the symbol closest to y_i that has 1 in the k-th bit position
(and similarly for $s_{o,k} \text{ min}$).

should read

...dropped and $s_{\text{min}}^{1,k}$ denotes the symbol closest to y_i that has 1 in the k-th bit position
(and similarly for $s_{\text{min}}^{o,k}$).

Signed and Sealed this

Seventh Day of July, 2009



JOHN DOLL
Acting Director of the United States Patent and Trademark Office